

1. (Amended) A display device comprising:

a first substrate;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction,

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

a first insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates;

a first plurality of conductive layers interposed between said first substrate and said sealing member, said first plurality of conductive layers comprising a same material as said plurality of scanning lines;

a second plurality of conductive layers interposed between said first substrate and said sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines; and

a second insulating film disposed between said first plurality of conductive layers and said second plurality of conductive layers wherein said insulating film comprises a same material as said interlayer insulating film,

wherein said first plurality of conductive layers and said second plurality of conductive layers are arranged in turn so that said second plurality of conductive layers do not overlap said first plurality of conductive layers.

Please add new claims 77-95.

77. A display device comprising:

a first substrate having at least a first side edge and a second side edge;

a plurality of first conductive lines extending over the first substrate in a first direction;

a plurality of second conductive lines extending over the first substrate in a second direction orthogonal to said first direction;

a plurality of thin film transistors disposed at each intersection of said first conductive lines and said second conductive lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

a first insulating film disposed between said first conductive lines and said second conductive lines;

a second substrate opposed to said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a first portion along said first side edge and a second portion along said second side edge;

a first plurality of conductive layers interposed between said first substrate and the first portion of said sealing member, said first plurality of conductive layers comprising a same material as said plurality of first conductive lines;

a second plurality of conductive layers interposed between said first substrate and the first portion of said sealing member, said second plurality of conductive layers comprising a same material as said plurality of second conductive lines;

a second insulating film disposed between said first plurality of conductive layers and said second plurality of conductive layers wherein said insulating film comprises a same material as said first insulating film; and

a third conductive layer comprising a same material as said plurality of second lines and interposed between said second portion of the sealing member and said first substrate,

wherein said first plurality of conductive layers and said second plurality of conductive layers are arranged in turn, and

wherein said third conductive layer continuously extends along said second side edge for a length longer than a pitch of said second lines.

78. The display device according to claim 77 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

79. The display device according to claim 77 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

80. The display device according to claim 77 wherein said third conductive layer is electrically isolated from either of said plurality of first conductive lines and said plurality of second conductive lines.

81. The display device according to claim 77 wherein said third conductive layer extends in a form of a rectangular wave.

82. A display device comprising:

a first substrate having at least a first side edge and a second side edge;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

a second substrate opposed to said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a first portion adjacent to said first side edge and a second portion adjacent to said second side edge;

a first plurality of conductive layers interposed between said first substrate and the first portion of said sealing member, said first plurality of conductive layers comprising a same material as said plurality of scanning lines;

a second plurality of conductive layers interposed between said first substrate and the first portion of said sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines;

a third conductive layer interposed between said second portion of the sealing member and said first substrate, said

third conductive layer comprising a same material as said plurality of scanning lines,

wherein said first plurality of conductive layers and said second plurality of conductive layers are arranged in turn and isolated from each other; and

wherein said third conductive layer continuously extends along said second side edge for a length longer than a pitch of the adjacent ones of said lines.

83. The display device according to claim 82 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

84. The display device according to claim 82 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

85. The display device according to claim 82 wherein said third conductive layer is electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

86. The display device according to claim 82 wherein said third conductive layer extends in a form of a rectangular wave.

87. A display device comprising:

a first substrate having a first side edge and a second side edge;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

an interlayer insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a first portion adjacent to the first edge of the substrate and a second portion adjacent to the second edge of the substrate wherein said plurality of scanning lines extend below and beyond the first portion of the sealing member;

a plurality of first conductive layers interposed between said first substrate and the second portion of said sealing

member, said plurality first of conductive layers comprising a same material as said plurality of scanning lines;

a plurality of second conductive layers interposed between said first substrate and the second portion of said sealing member, said plurality of second conductive layers comprising a same material as said plurality of signal lines wherein said plurality of first conductive layers and said plurality of second conductive layers are arranged in turn;

a plurality of third conductive layers interposed between said first substrate and said first portion of the sealing member, said plurality of third conductive layers comprising a same material as said plurality of signal lines;

wherein said plurality of first conductive layers are disposed between a portion said plurality of scanning lines below said first portion of the sealing member.

88. The display device according to claim 87 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

89. The display device according to claim 87 wherein each channel region of said plurality of thin film transistors has a crystalline structure.



90. The display device according to claim 87 wherein said conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

91. A display device comprising:

a first substrate;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

a first insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to the first substrate;

a sealing member disposed at a periphery of said first and second substrates;

a first plurality of conductive layers interposed between said first substrate and said sealing member, said first plurality of conductive layers comprising a same material as said plurality of scanning lines;

a second plurality of conductive layers interposed between said first substrate and said sealing member, said second plurality of conductive layers comprising a same material as said plurality of signal lines; and

a second insulating film disposed between said first plurality of conductive layers and said second plurality of conductive layers,

wherein said first plurality of conductive layers and said second plurality of conductive layers are arranged in turn.

92. The display device according to claim 91 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

93. The display device according to claim 91 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

94. The display device according to claim 91 wherein said first plurality of conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.

95. The display device according to claim 91 wherein said second plurality of conductive layers are electrically isolated from either of said plurality of scanning lines and said plurality of signal lines.